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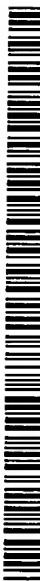
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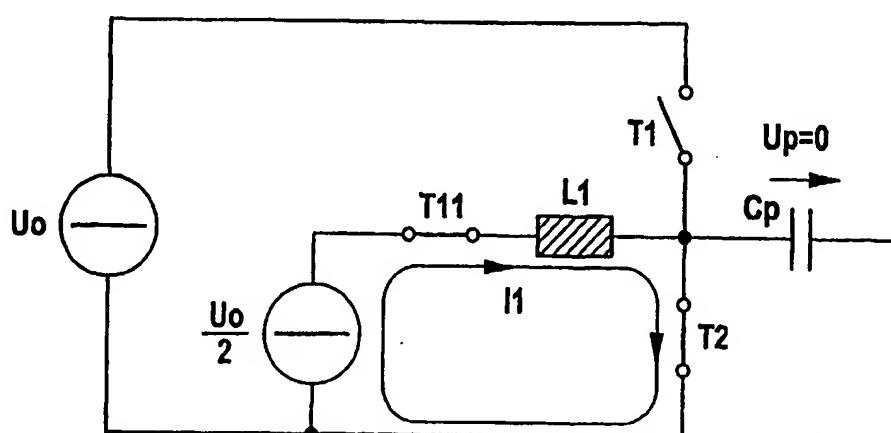
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(54) Title: METHOD OF CONTROLLING A CIRCUIT ARRANGEMENT FOR THE AC POWER SUPPLY OF A PLASMA DISPLAY PANEL



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(57) Abstract: A method of controlling a circuit arrangement for an AC voltage supply of a plasma display panel, the circuit arrangement comprising at least a transistor bridge constituted by the bridge transistors (T1, T2, T3, T4), an input voltage (U0), a capacitor (Cp) of the plasma cell and a charging circuit comprising an auxiliary voltage (Uh), a first auxiliary transistor (T11) and a first coil (L1) and at the beginning of the charging operation the first auxiliary transistor (T11) has been turned on, characterized in that once the first auxiliary transistor (T11) has been turned on, the second bridge transistor (T2) of the half bridge continues to be turned on for a delay time  $t_v$  and is turned off after the delay time  $t_v$  has elapsed.